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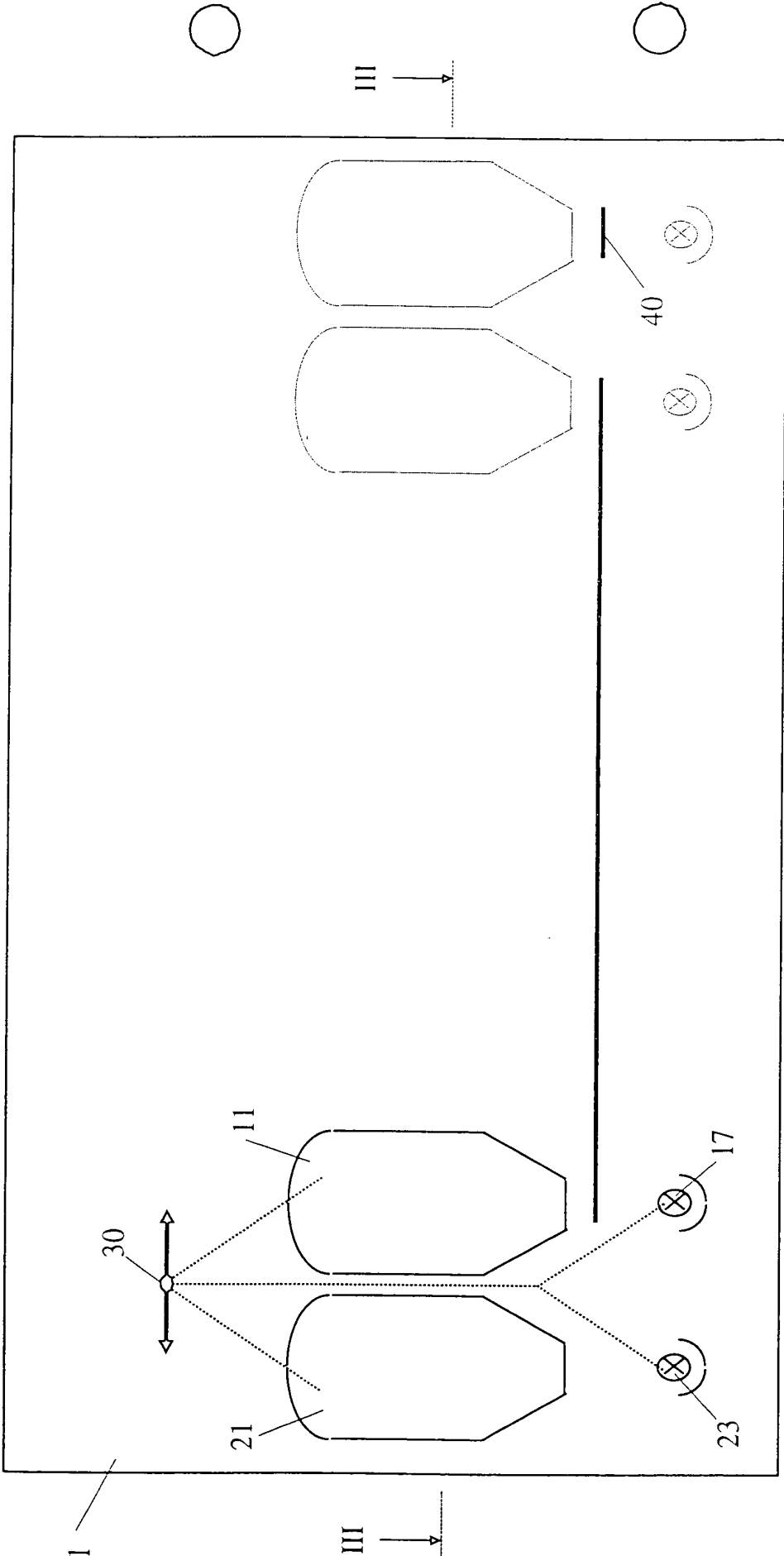


Fig. 2

Fig. 3 is a schematic diagram of a system for processing data. The system includes a central processing unit (CPU) 10, a memory unit 11, a storage unit 12, and a control unit 13. The CPU 10 is connected to the memory unit 11 and the storage unit 12. The control unit 13 is connected to the CPU 10. The system is designed to process data and store it in the storage unit 12.

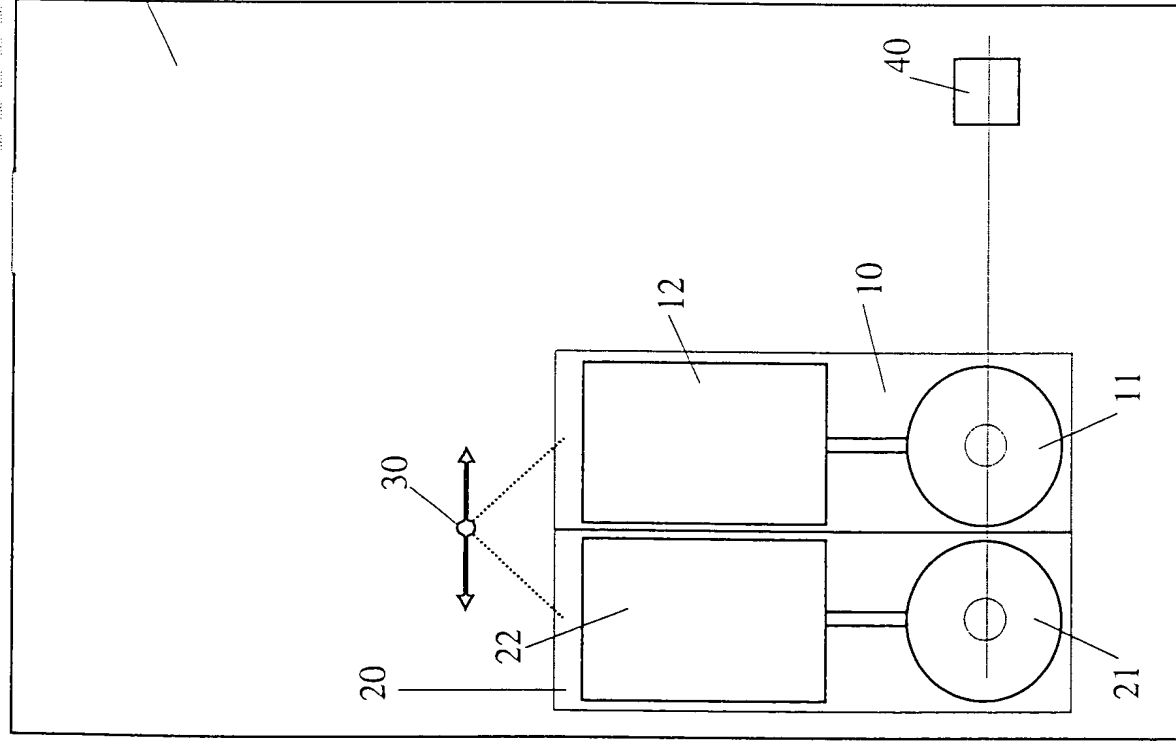


Fig. 3

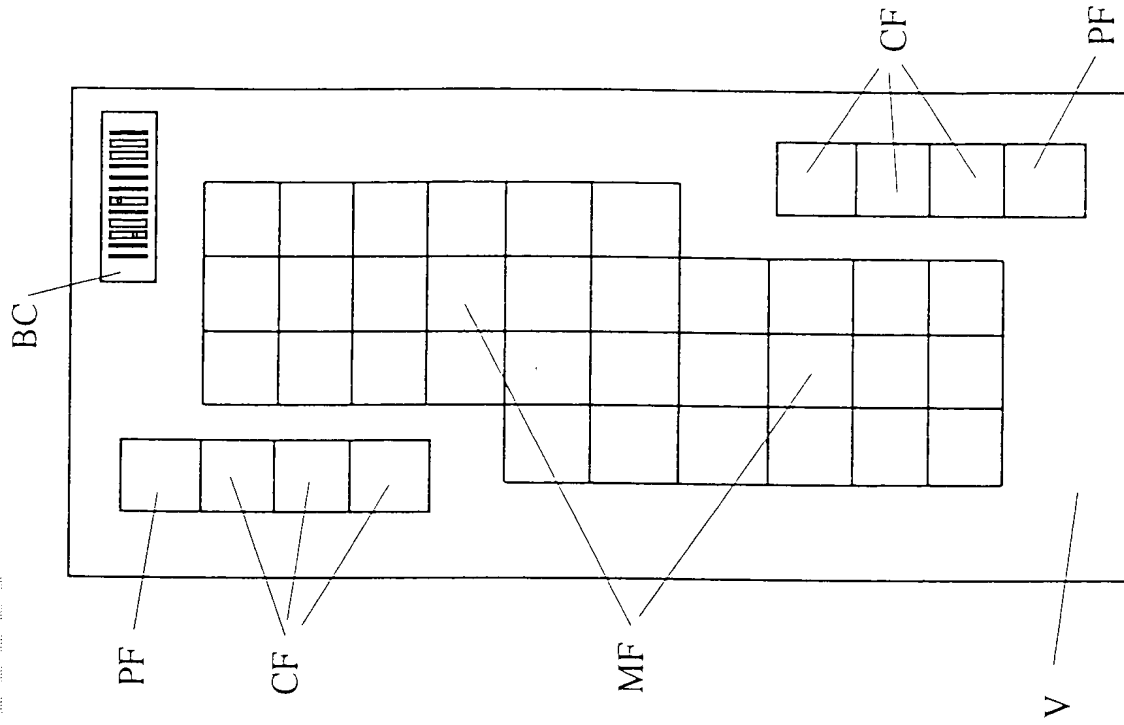


Fig. 5

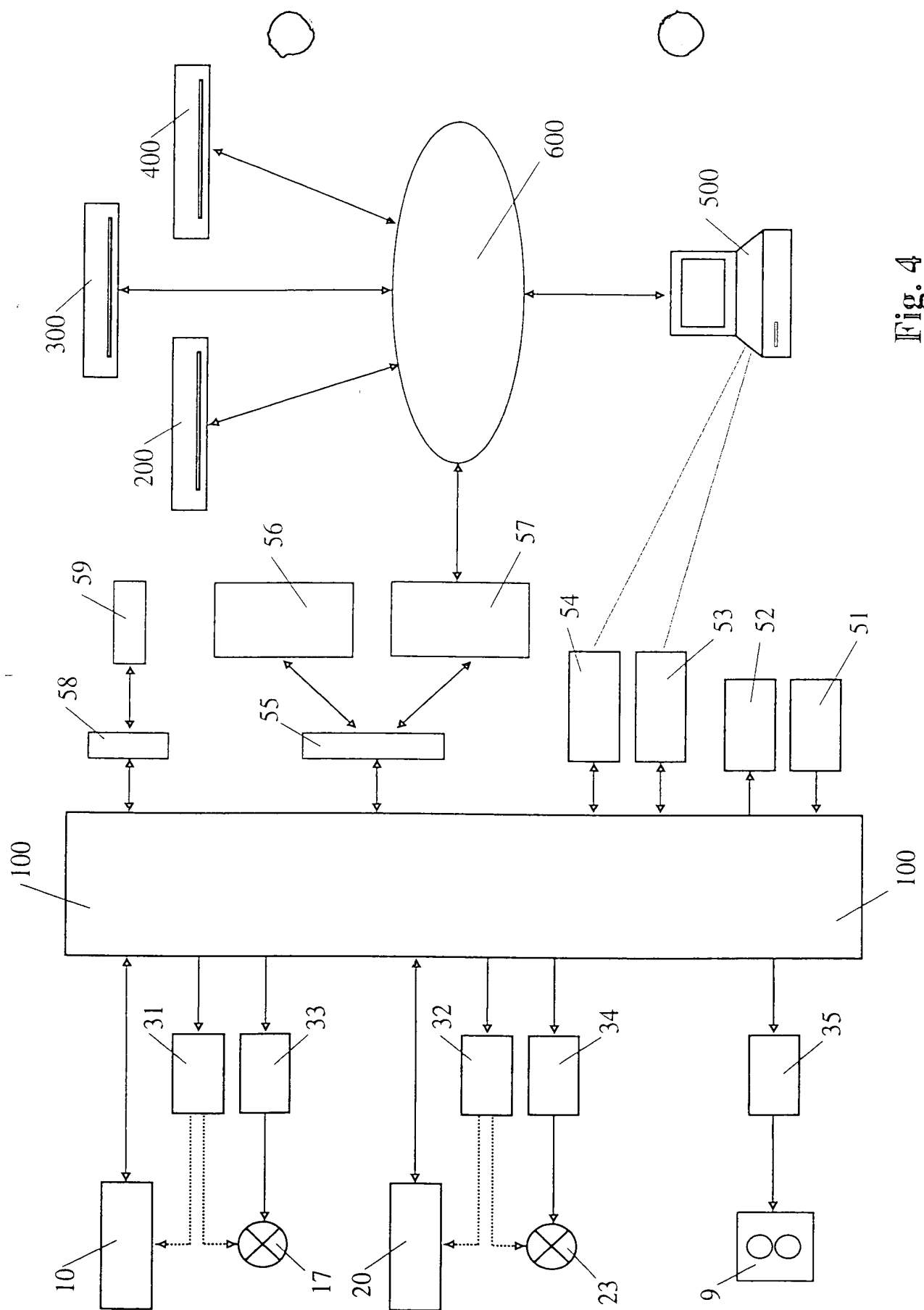


Fig. 4